



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
[www.uspto.gov](http://www.uspto.gov)



CONFIRMATION NO. 4517

Bib Data Sheet

SERIAL NUMBER 10/828,408	FILING DATE 04/19/2004 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 03-2644 81693
-----------------------------	-----------------------------------	--------------	------------------------	---

## APPLICANTS

Viswanathan Lakshmanan, Thornton, CO;

Richard D. Blinne, Ft. Collins, CO;  
Jonathan P. Kuppinger, Windsor, CO;

## \*\* CONTINUING DATA \*\*\*\*\*

O.K. VERIFIED S.P.

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

O.K. VERIFIED S.P.

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 06/30/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	<i>Sudhir Parikh S.P.</i> Examiner's Signature Initials	CO	4	10	2

## ADDRESS

24319  
 LSI LOGIC CORPORATION  
 1621 BARBER LANE  
 MS: D-106  
 MILPITAS , CA  
 95035

## TITLE

Method and computer program for verifying an incremental change to an integrated circuit design

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue )
RECEIVED 770		

<input type="checkbox"/> Other _____
<input type="checkbox"/> Credit
